

## **DETAILED ACTION**

### ***Remarks***

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 1/17/2008 has been entered.

### ***Response to Arguments***

2. Applicant's arguments see Remark pages 6-7, filed 1/17/2008, with respect to claims 1-3 and 5-7 have been fully considered and are persuasive. Claims 1-3 and 5-12 are allowed.

### ***Examiner's Amendment***

3. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

4. Authorization for this examiner's amendment was given in a telephone interview with John C. Donch, Attorney for Applicants, Reg. No. 43,593 on 4/3/2008.

5. The application is amended as follows:
- replace phrase "claim 1" with phrase -- claim 10 -- in line1 of claims 11 and 12.

***Allowable Subject Matter***

6. Claims 1-3 and 5-12 are allowed. The following is an examiner's statement of reasons for allowance:

The prior arts of record fail to teach or suggest in combination, the steps of **"receiving a stable high frequency reference signal; dividing said high frequency reference signal which allows a clock signal to have one of a plurality of clock phases; receiving pseudorandom number (PN) clock signal and providing PN phase adjustments of said received PN clock signal;"**, in combination with **"generating a tracking control signal in response to said PN phase adjustments for adjusting said system clock phase to one of the plurality of available phases; and adjusting said clock phase in accordance with the tracking control signal to provide said synchronized clock signal"** in order to form "A METHOD FOR PROVIDING A SYNCHRONIZED CLOCK SIGNAL HAVING REDUCED JITTER", as recited in claim 1, and some variation of wording as recited in claims 7 and 10, and further limitation of their respective dependent claims 2-3, 5-6, 8-9, and 11-12 correspondingly.

7. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

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accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

***Contact Information***

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to NADER BOLOURCHI whose telephone number is (571)272-8064. The examiner can normally be reached on M-F.
9. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David. C. Payne can be reached on 571-272-3024. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300.
10. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at (866) 217-9197 (toll-free).

/N. B./

Examiner, Art Unit 2611

/David C. Payne/  
Supervisory Patent Examiner, Art Unit 2611

**Application Number****Application/Control No.**

10/706,369

**Applicant(s)/Patent under  
Reexamination**

KAEWELL, JOHN

**Examiner**

NADER BOLOURCHI

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